

SEMITOP®E2

Sixpack Open Emitter Engineering Sample SK75GD06E3ETE2

Target Data

Features*

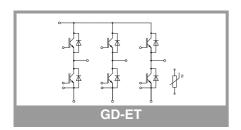
- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 650V Trench IGBT3 (E3)
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- · Motor drives
- Servo drives
- · Air conditioning
- · Auxiliary Inverters
- UPS

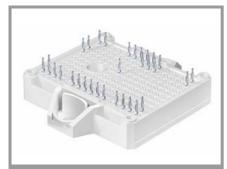
Remarks

• Recommended $T_{j,op}$ =-40 ...+150 °C



Absolute Maximum Ratings							
	Conditions		Values	Unit			
Inverter - I			raido	0			
V _{CES}	T _i = 25 °C		650	V			
I _C	$\lambda_{\text{paste}} = 0.8 \text{ W/(mK)}$	T _s = 25 °C	74	A			
-0	T _j = 175 °C	T _s = 70 °C	59	Α			
I _C	λ _{paste} =2.5 W/(mK)		92	Α			
	T _j = 175 °C	T _s = 70 °C	75	Α			
I _{Cnom}		1 -	75	Α			
I _{CRM}			150	Α			
V _{GES}			-20 20	V			
t _{psc}	$V_{CC} = 360 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 650 \text{ V}$	T _j = 150 °C	6	μs			
Tj			-40 175	°C			
Inverse - E	Diode						
V_{RRM}	T _j = 25 °C		600	V			
I _F	λ _{paste} =0.8 W/(mK)	T _s = 25 °C	51	Α			
	T _j = 175 °C	T _s = 70 °C	41	Α			
l _F	λ _{paste} =2.5 W/(mK)	T _s = 25 °C	61	Α			
T _j = 175 °C	T _s = 70 °C	49	Α				
I _{FRM}			150	Α			
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 150 °C		395	Α			
Tj			-40 175	°C			
Module							
I _{t(RMS)}	, ΔT _{terminal} at PCB j	oint = 30 K, per pin	30	Α			
T _{stg}	module without TIM	1	-40 125	°C			
V _{isol}	AC, sinusoidal, t = 1 min		2500	V			

Characteristics								
Symbol	Conditions		min.	typ.	max.	Unit		
Inverter -	IGBT							
V _{CE(sat)}	$I_C = 75 A$	T _j = 25 °C		1.45	1.77	V		
	V _{GE} = 15 V chiplevel	T _j = 150 °C		1.70	2.15	٧		
V _{CE0}	chiplevel	T _j = 25 °C		0.75	0.90	V		
		T _j = 150 °C		0.68	0.83	V		
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		9.3	12	mΩ		
	chiplevel	T _j = 150 °C		14	18	mΩ		
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_{C} = 1.2$	mA	5	5.8	6.5	V		
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 60$	00 V, T _j = 25 °C			0.1	mA		
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		4.62		nF		
Coes		f = 1 MHz		0.288		nF		
C _{res}		f = 1 MHz		0.137		nF		
Q_{G}	V _{GE} = - 15 V+ 15 V			470		nC		
R _{Gint}	T _j = 25 °C			0		Ω		
t _{d(on)}	V _{CC} = 300 V	T _j = 150 °C		32		ns		
t _r	$I_C = 75 \text{ A}$	T _j = 150 °C		38		ns		
Eon	$R_{G \text{ on}} = 6 \Omega$ $R_{G \text{ off}} = 6 \Omega$	T _j = 150 °C		2		mJ		
t _{d(off)}		T _j = 150 °C		363		ns		
t _f		T _j = 150 °C		38		ns		
E _{off}	V _{GE} = +15/-15 V	T _j = 150 °C		2.8		mJ		
R _{th(j-s)}	per IGBT, λ _{paste} =0.	8 W/(mK)		0.91		K/W		
R _{th(j-s)}	per IGBT, λ _{paste} =2.	5 W/(mK)		0.63		K/W		



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Typical Applications

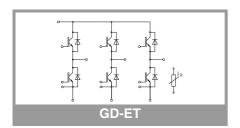
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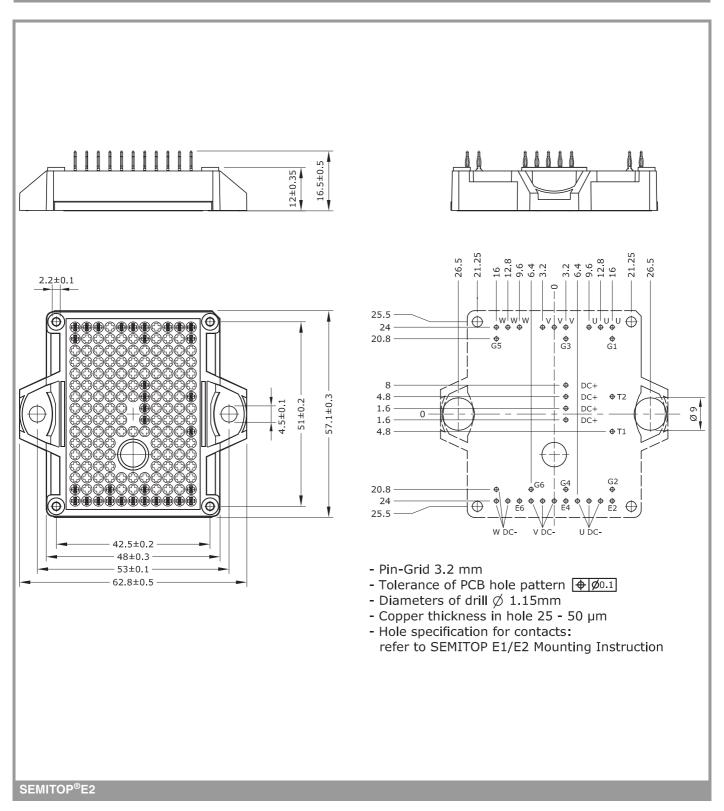
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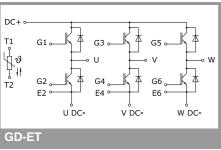
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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse -						•
$V_F = V_{EC}$	I _F = 75 A	T _j = 25 °C		1.50	1.73	V
	chiplevel	T _j = 150 °C		1.54	1.76	V
V_{F0}	chiplevel	T _j = 25 °C		1.00	1.10	V
	Chipievei	T _j = 150 °C		0.85	0.95	V
r _F	F shiployel	T _j = 25 °C		6.7	8.3	mΩ
chiplevel	Chipievei	T _j = 150 °C		9.2	11	mΩ
I _{RRM}	I _F = 75 A	T _j = 150 °C		52		Α
Q _{rr}	$V_{GE} = -15 \text{ V}$ $V_{CC} = 300 \text{ V}$	T _j = 150 °C		8		μC
E _{rr}		T _j = 150 °C		1.45		mJ
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			1.26		K/W
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			0.93		K/W
Module						
L _{CE}				40		nΗ
Ms	to heatsink		1.6		2.3	Nm
w				35		g

Characteristics							
Symbol	Conditions min. typ. max.		max.	Unit			
Temperature Sensor							
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)	493 ± 5%		Ω			
B _{25/85}	$R_{(T)} = R_{25} * \exp[B_{25/85} * (1/T-1/298)], T[K]$	3420		K			







This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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